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MAY 24 2001

Total number of pages in this submission

2

Application Number	09/344,169
Filing Date	June-24, 1999
First Named Inventor	William Eric Corr
Group Art Unit	2763
Examiner Name	

Attorney Docket Number

await / P-3376

Technology Center 2100

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## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Individual Name	Leo Peters, Reg. No. 33,562, Phone: [+1] 408-433-4578
Signature	
Date	5-18-01

## CERTIFICATE OF MAILING

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

William Eric Corr

Serial No.: 09/344,169

Group Art Un 2763

Filed: June 24, 1999

For: Determining Timing of Integrated Circuits

Atty Docket: await / P-3376

Technology Center 2100

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Assistant Commissioner for Patents  
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Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

/

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

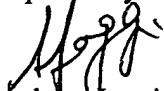
The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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Date: 5/18/01

Respectfully submitted,

  
Sandeep Jaggi  
Chief IP Counsel  
Corporate Assistant Secretary  
LSI LOGIC CORPORATION